

What is claimed is:

1. A signal switching apparatus that assigns a plurality of signals input to a mix bus comprising a plurality of buses, to respective desired output channels assigned to respective ones of the buses, and outputs the plurality of signals via the output channels, comprising:  
a plurality of checking signal generating devices that generate checking signals different from each other;  
and  
a checking signal input device that causes the checking signals generated by said plurality of checking signal generating devices to be selectively input to respective desired buses of the mix bus.
2. A signal switching apparatus according to claim 1, wherein the plurality of buses are grouped into a plurality of groups.
3. A signal switching apparatus according to claim 2, wherein said checking signal input device comprises a selecting device that selects desired checking signals to be input to part of the plurality of groups from among the plurality of checking signals, and wherein the checking signals selected by said selecting device are input to all of the buses belonging to the part of the plurality of groups.
4. A signal switching apparatus according to claim 2, wherein said checking signal input device causes part of the plurality of checking signals to be always input to all of buses belonging to a predetermined part of the plurality of groups.
5. A program executed by a computer, for assigning a plurality of signals input to a mix bus comprising a plurality of buses, to desired output channels assigned to respective ones of the buses and outputting the plurality of signals via the output channels, comprising:

a checking signal generating module for generating a plurality of checking signals different from each other; and

- 5 a checking signal input module for causing the plurality of checking signals generated by said checking signal generating module to be selectively input to respective desired buses of the mix bus.